

u/v

o/w

o/w



8 7 6 5 4 3 2 1
1 2 3 4 4 3 2 1

PO

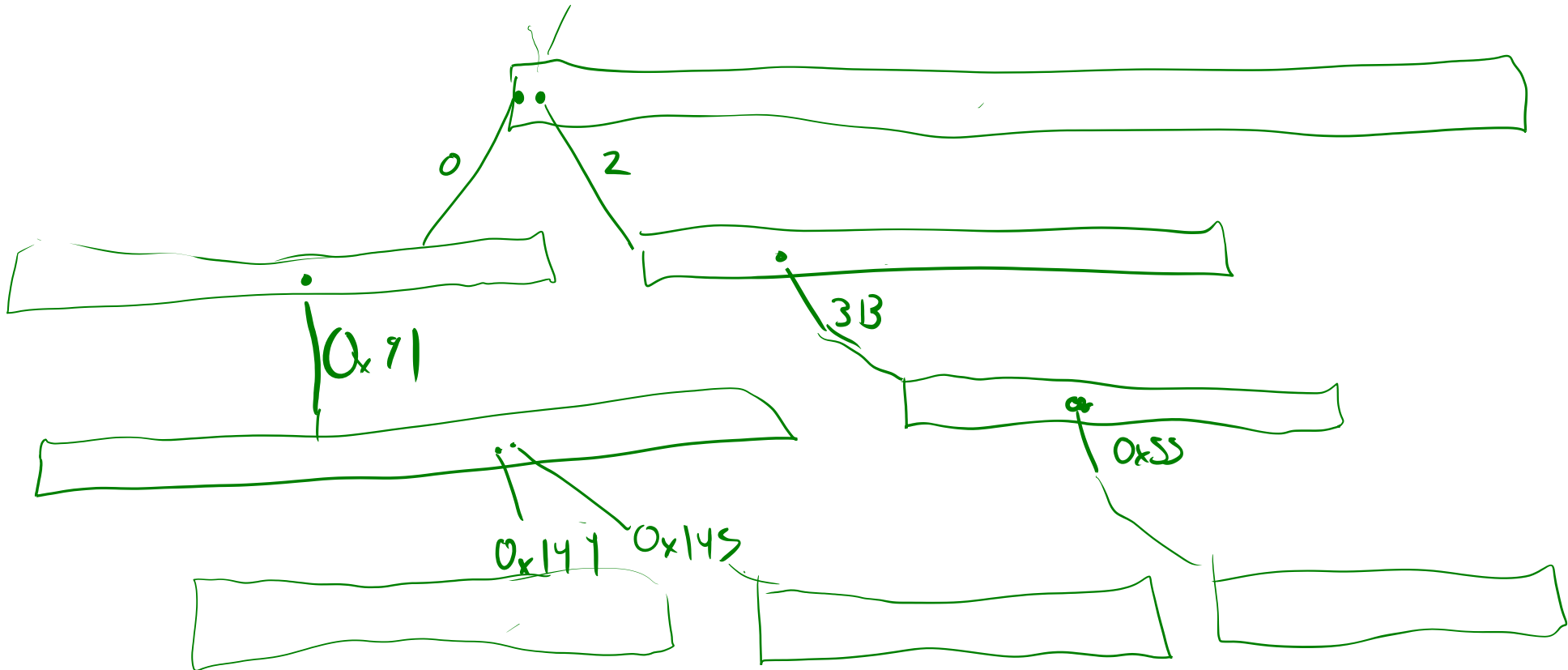
0001 0010 0011 0100 0100 0011 0010 0001

0

0101 0110 0111 1000

1000 0111 0110 0101 0101 0110 0111 1000

2





Hardware address
MAC address 48 bit integer