1. Read Chapter 5
2. Complete the following problems from the textbook:
   For problems 5.3, 5.8, and 5.9b, use the diagramming method shown in class:
   - Each stage (IF, ID, EXE, MEM, WB) is represented as a box
   - Label each stage with the register transfer activity that occurs
   - Draw pipeline bubbles as shown in class

   - 5.3
   - 5.4
   - 5.8
   - 5.9
   - 5.15