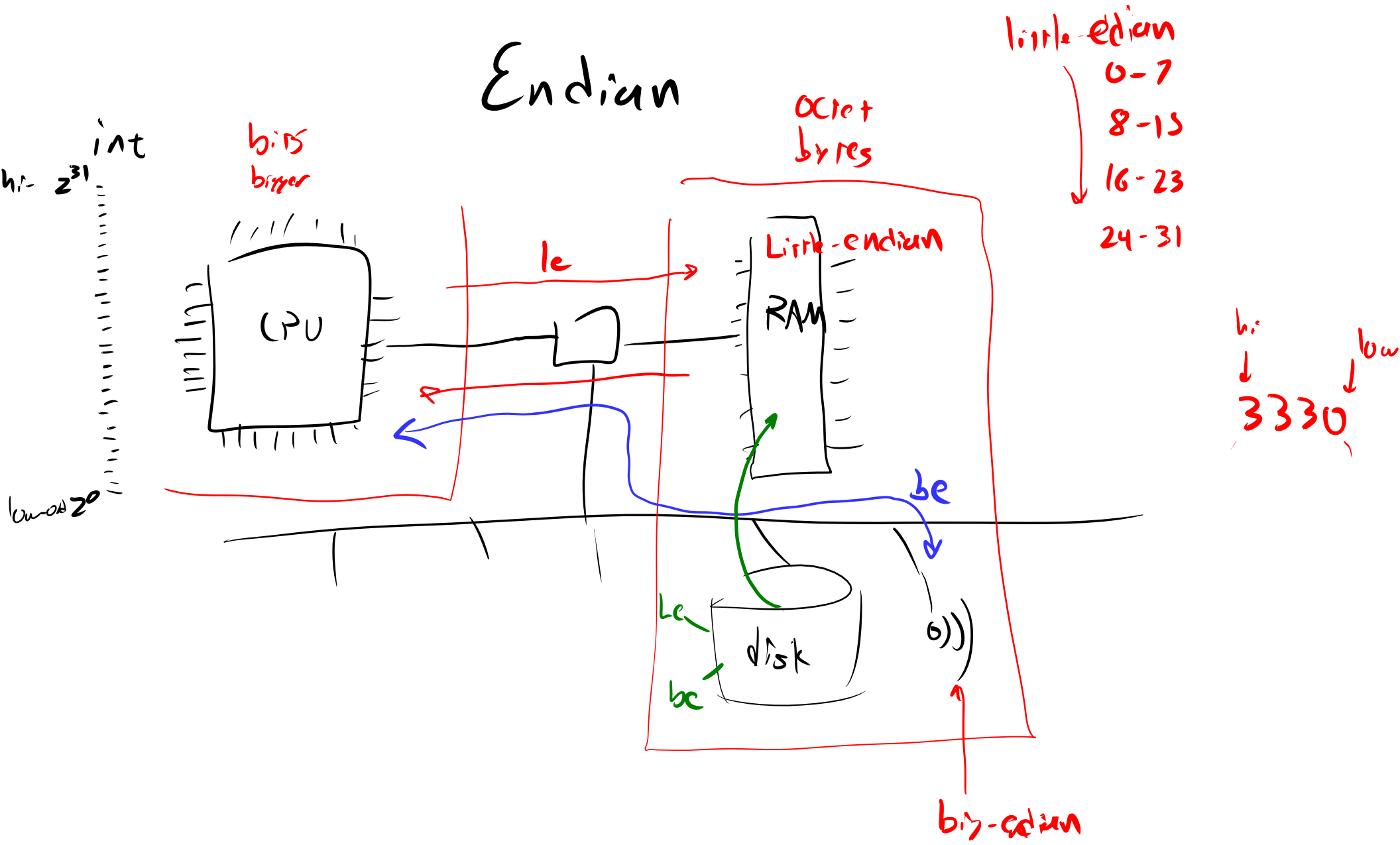
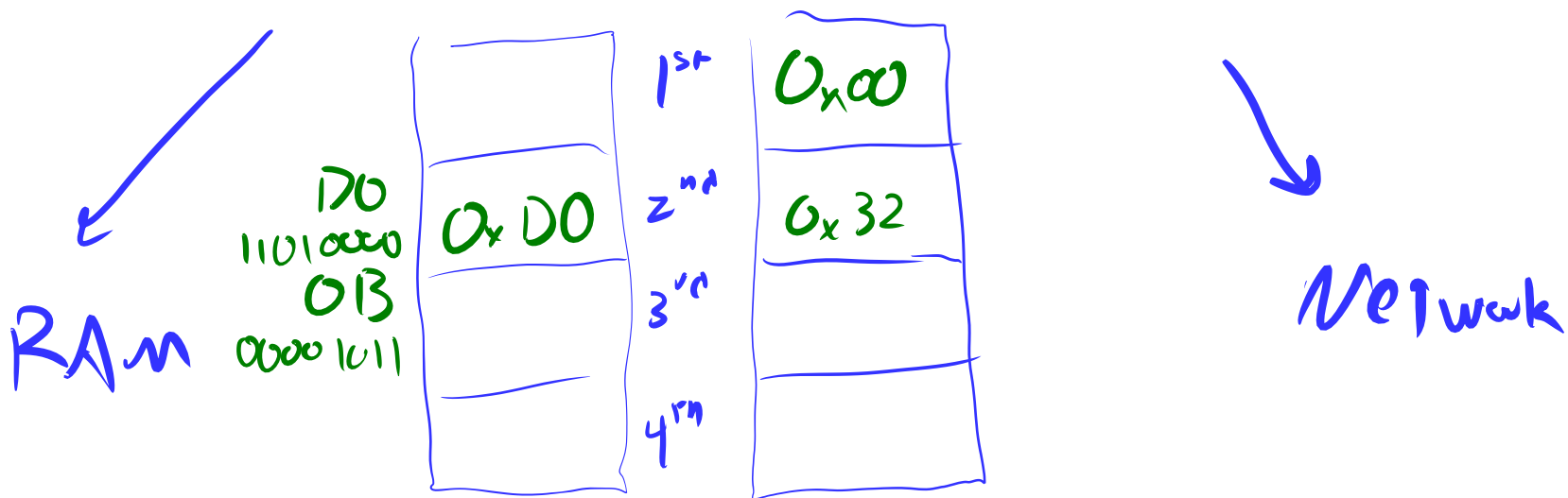
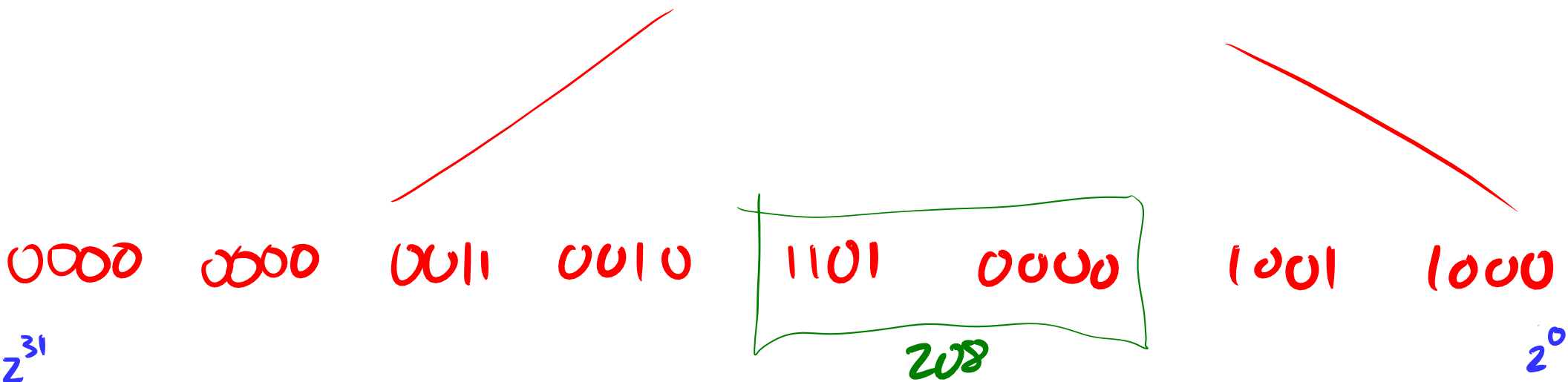




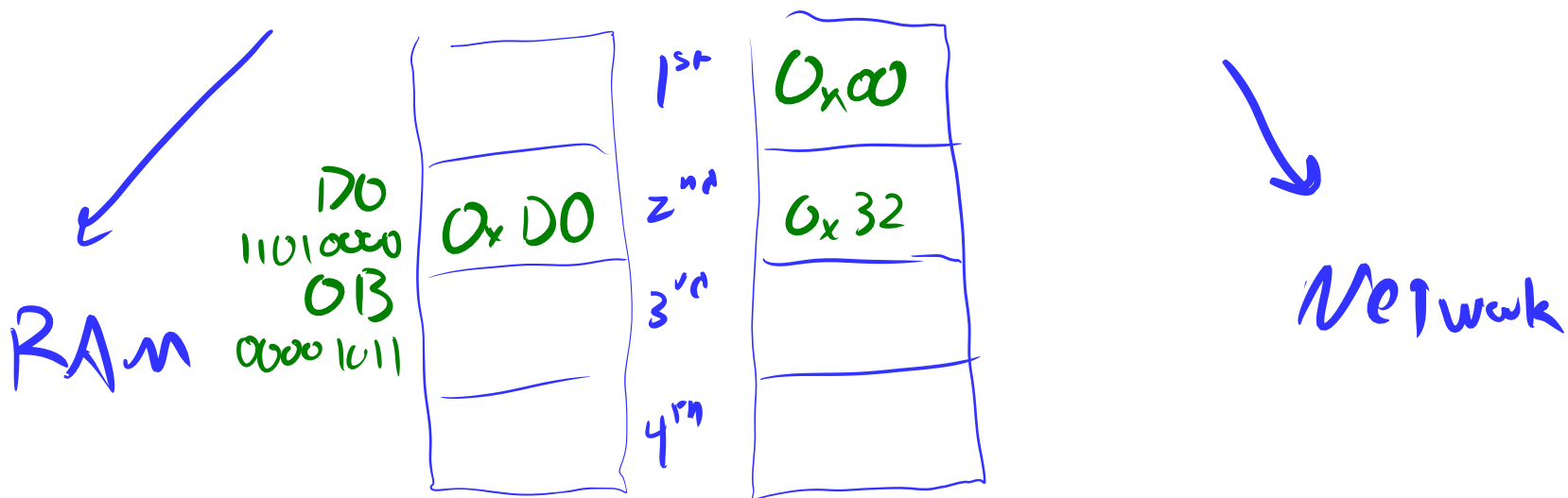
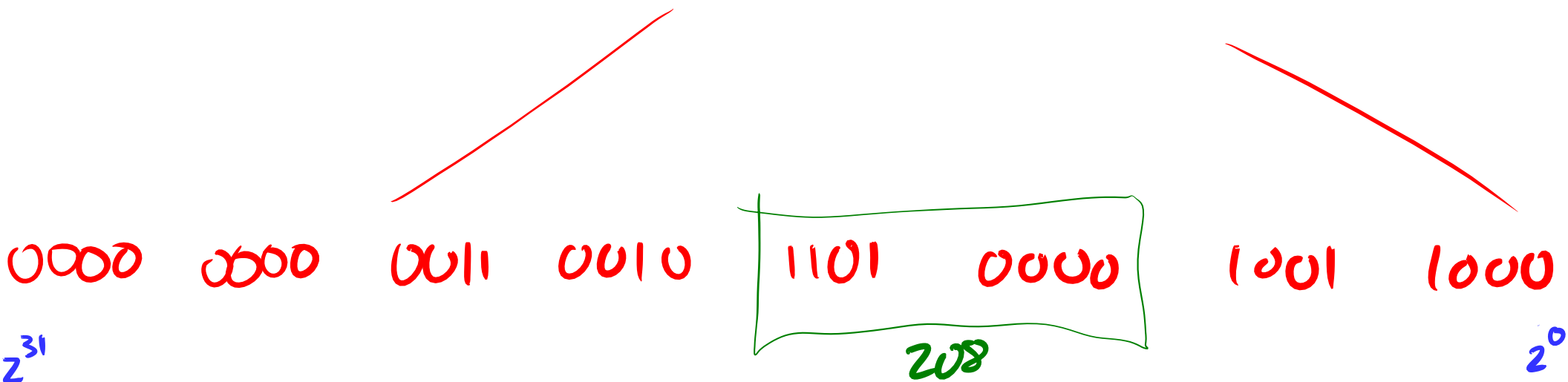
# Endian



int x = 0x32D098



int x = 0x32D098



# Instruction set Architecture

x86-64

Intel / Microsoft  
ZISO

even more else  
our text book

suffix  
 32-bit  
 64-bit



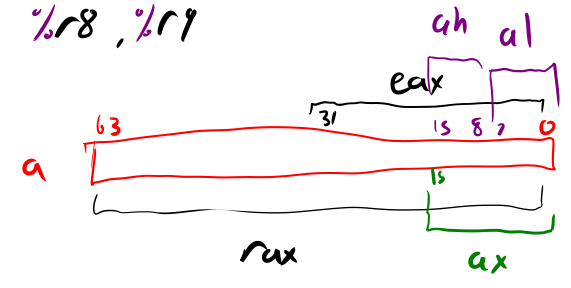
```
add %r8, %r9
```

immediate \$37 \$-0x23

register %rax

memory 37  
 (%rax)

37 (%rax) ~ memory [37 + rax]



```
long *a;
    a[i] = 3;
addq $3, (%rax, %rdi, 8)
```

37 (%r8, %r9, 8) ~ memory [37 + r8 + r9 \* 8]

