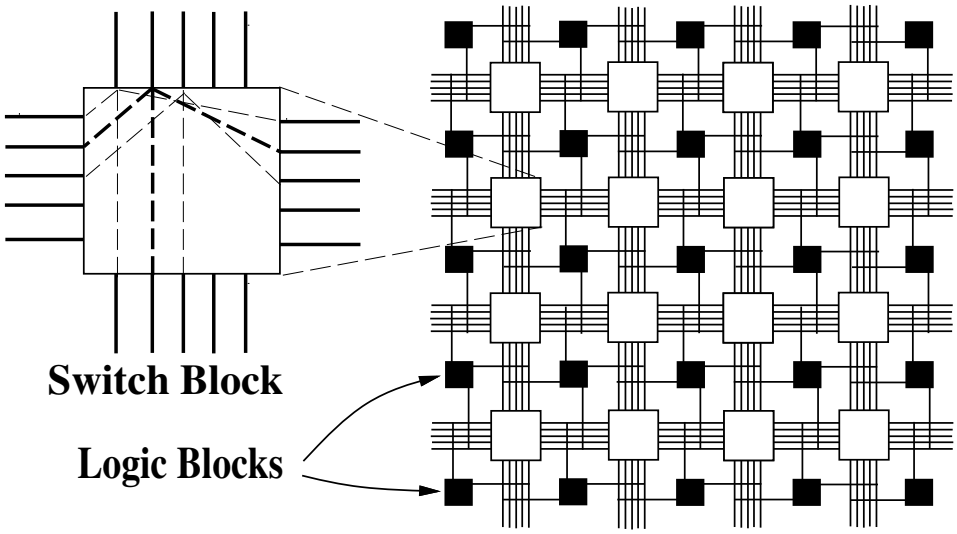

Figure 1. A symmetrical-array FPGA.