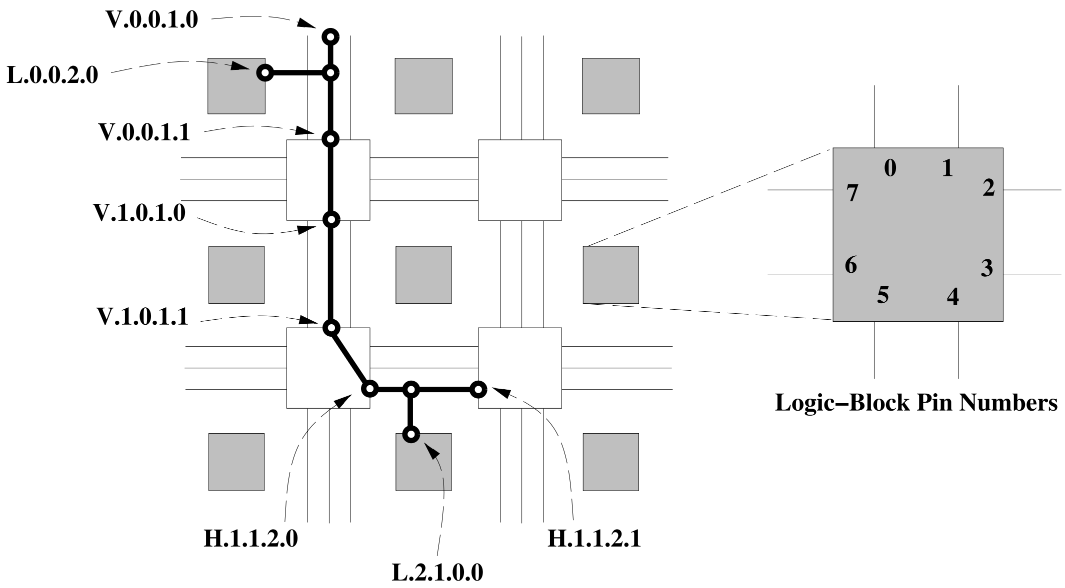

Example of labeling FPGA components. The darkly shaded segments correspond to a routing solution for the two-pin net. Shown at right is the numbering of logic-block pins 0 through 7, clock-wise from top left.