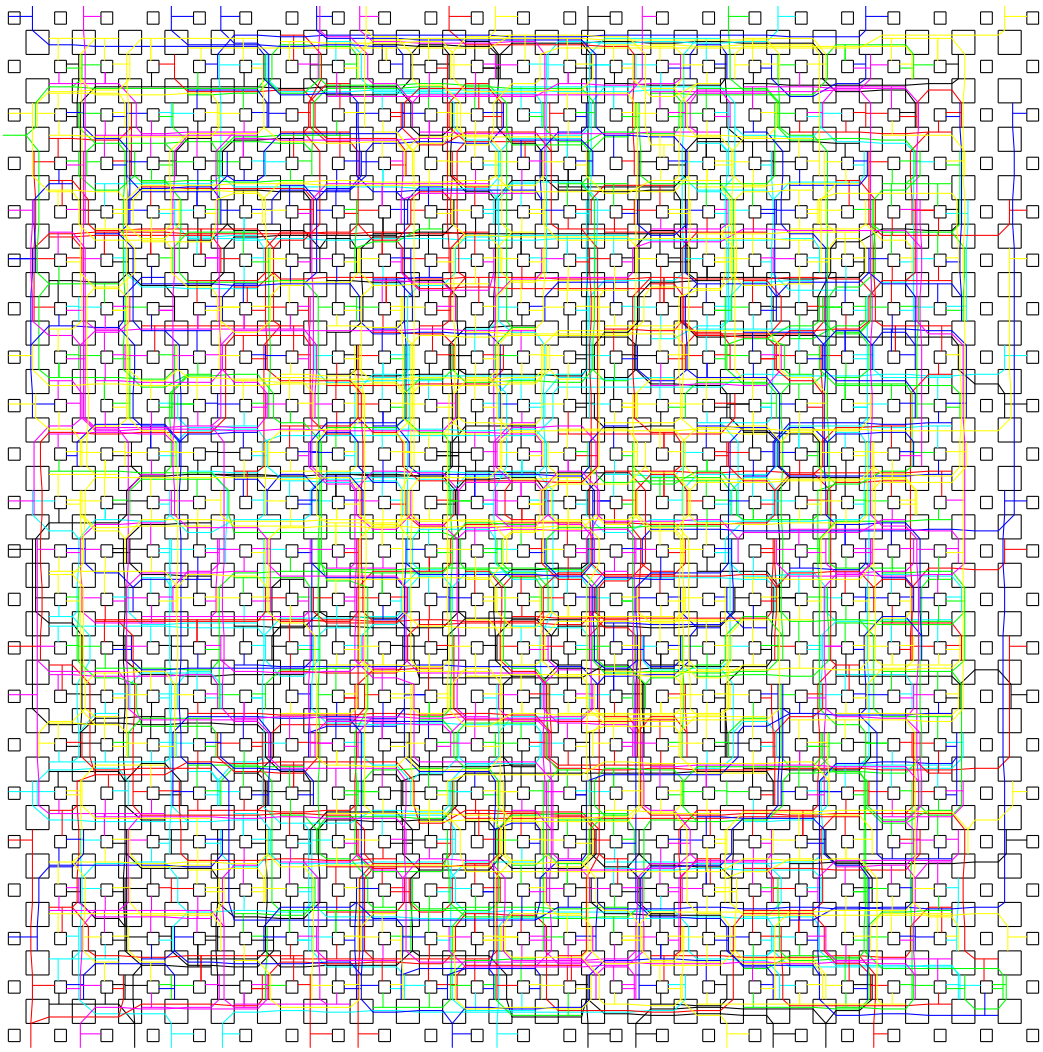

Circuit DFSM routed on X3000-type FPGA with $F_s = 6$, channel width $W = 9$, and $F_c = \lceil 0.6 \cdot W \rceil$, using the IKMB routing algorithm. Complete information is available on the World Wide Web at URL http://www.cs.virginia.edu/~robins/.