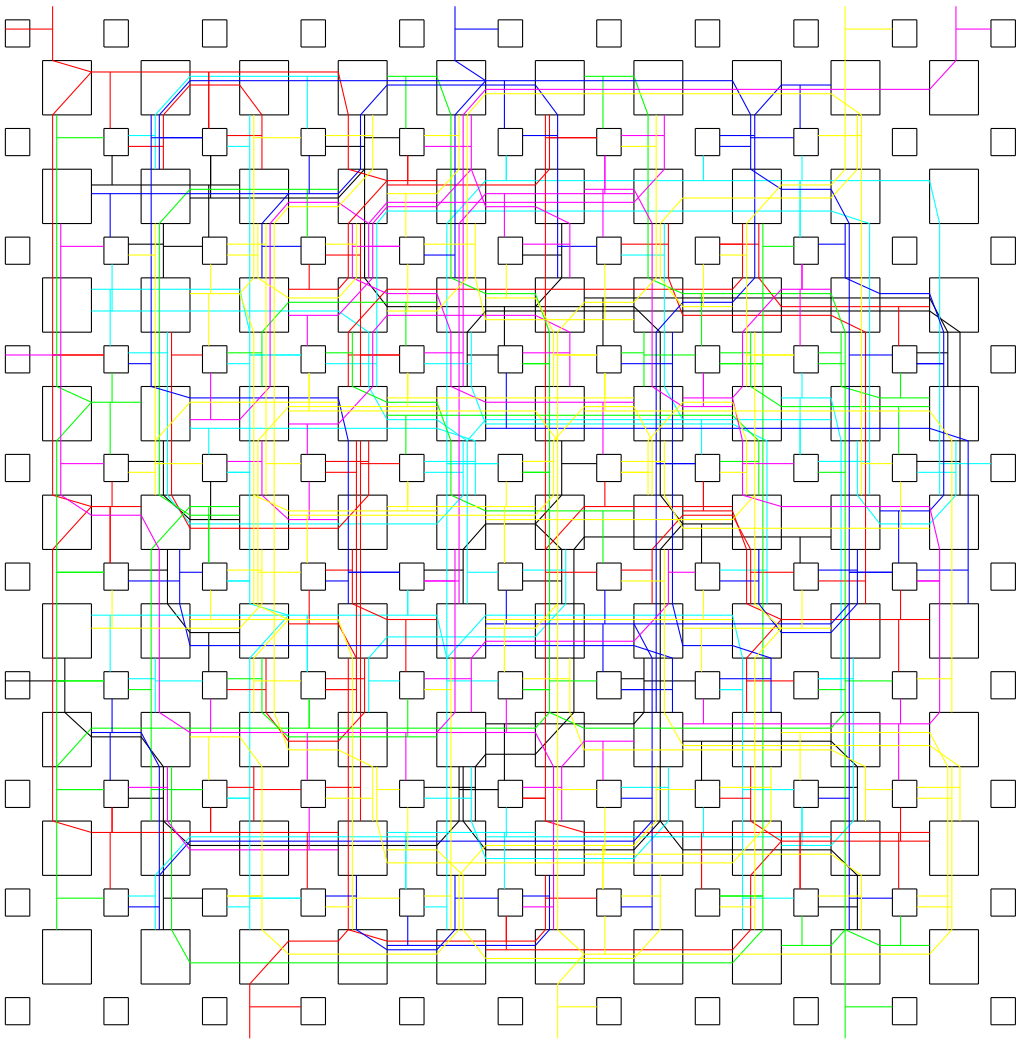

Circuit 9SYMML routed on X4000-type FPGA with $F_s = 3$, channel width $W = 8$, and $F_c = W$, using the IKMB routing algorithm. Complete information is available on the World Wide Web at URL http://www.cs.virginia.edu/~robins/.