CS 4330/6501 Computer Architecture Spring 2019
Homework 0: Student Information

Instructor: Samira Khan
TA: Sihang Liu

Homework Due: 5.00 pm, Jan 16, 2019

Please submit an electronic copy via collab. Submit as a pdf file. All future grading is dependent on the completion of this homework.

- Full name

- Department, major, degree

- When did you start at UVa?

- Do you have any research experience? If yes, provide a short summary of your research.

- What is your goal and career plan beyond the current degree you are working on?

- Do you have taken any undergraduate computer architecture course? What was the most interesting topic?

- Do you want to learn and come up with new ideas on how to design future microprocessors?

- Do you want to make any other comment (about anything)?

- Explain these terms: **ISA**, **Pipelining**, **Cache**, **Program Counter**, and **Register**. *If you are having hard time to remember these concepts, please revise prior materials from* `https://www.youtube.com/watch?v=zLP_X4wyHbY&list=PL5PHm2jkkXmi5CxxI7b3JCL1TWybTDtKq`.