

Q10

Overflow def

Q6

Q12

Q14

ISA simulator - kum inst

non-ir -

Q10

draw circuits - 2

Mux E Gates?

Q9

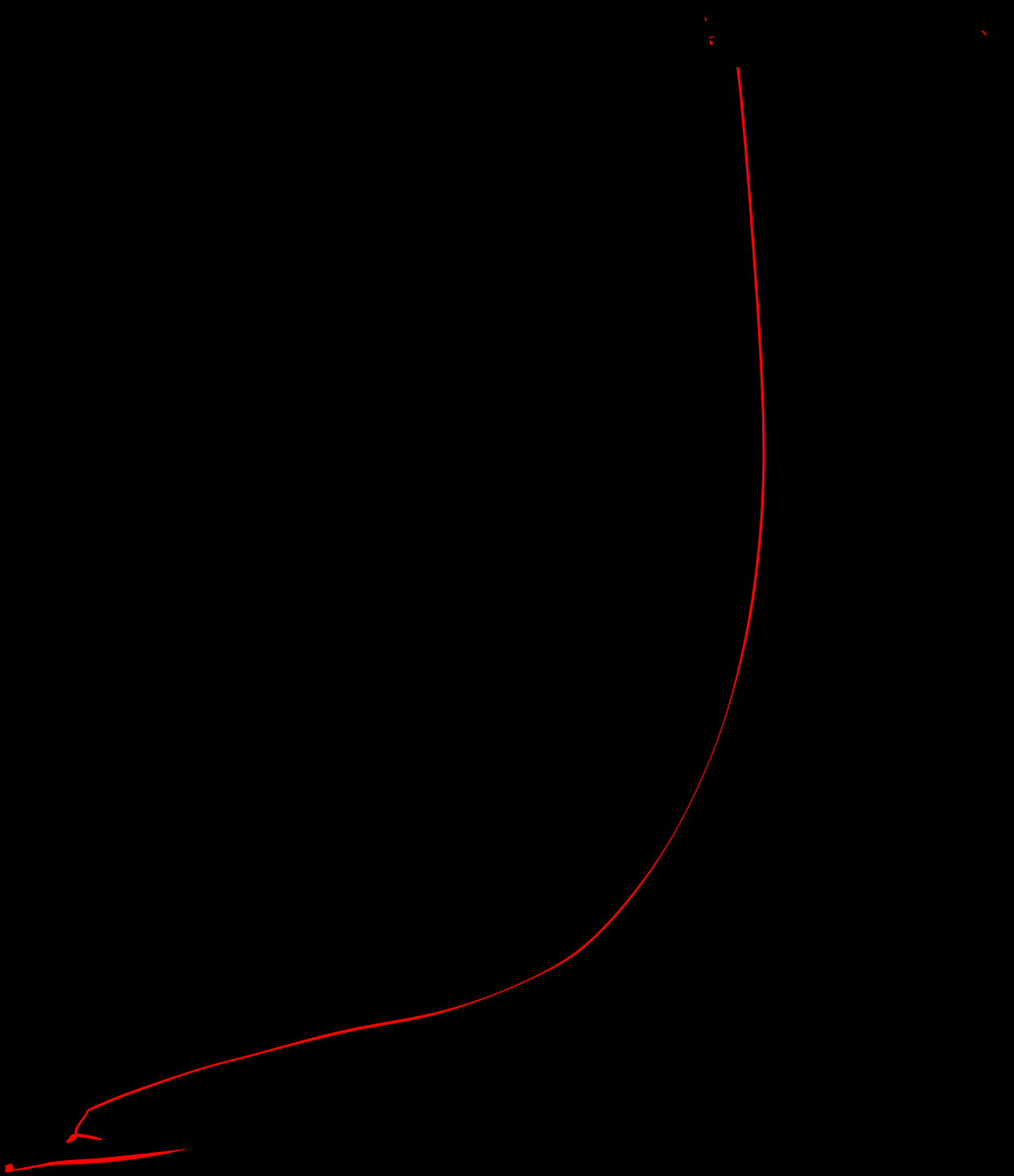
Q17

Q8

12 ISA memory & immediate

Q11

\* \* I hear



text, file



D becomes 0, then (lock to 0 & back to 1

$M[ \text{---} ] \text{ } M[ \text{---} ]$

$$R(a) = M(\text{address})$$

3

98bc

$[01, 23, 45, 67] \rightarrow 67\ 45\ 23\ 01$

1  
|  
0

1

0

0

0

0

0

0

1

$$x = 0 \cdot 0 \cdot 0 \cdot 0 \cdot 1 \cdot 0 \cdot 0 \cdot 0_2$$

8

2

4

8

→ 0110

14

→ 1110001

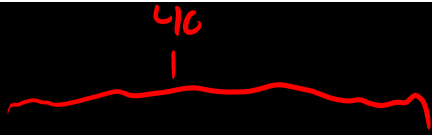
↑

$$x = 8$$

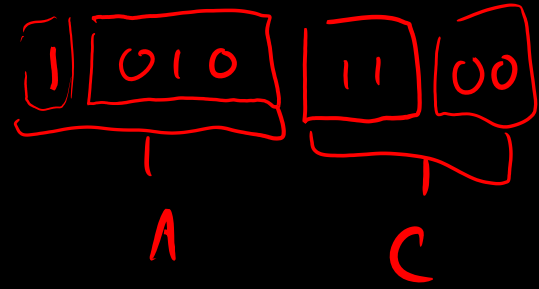




3



0



MC

34

7 MC

12

34

12





