

# Writing Code

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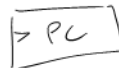
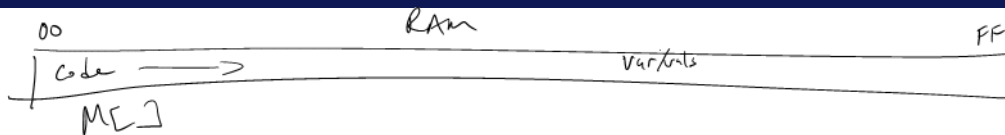
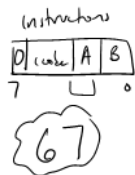
CS 2130: Computer Systems and Organization 1

September 21, 2022

# Announcements

- Homework 3 due Monday at 11pm on Gradescope
- Exam 1 next Friday (in class)

# Our CS2130 Machine



# High-level Instructions

In general, 3 kinds of instructions

- **moves** - move values around without doing “work”
- **math** - broadly doing “work”
- **jumps** - jump to a new place in the code

# Moves

Few forms

- Register to register (icode 0),  $x = y$   $r0 = r3$
- Register to/from memory (icodes 3-4),  $\underline{x} = \underline{M[b]}, M[b] = \underline{x}$

Memory

- Address: an index into memory.
  - Addresses are just (large) numbers
  - Usually we will not look at the number and trust it exists and is stored in a register

# Moves

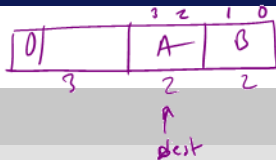


## Example 3-bit icode

icode	b	action
0		$rA = rB$
3		$rA =$ read from memory at address $rB$
4		write $rA$ to memory at address $rB$
5	3	$rA = pc$
6	0	$rA =$ read from memory at $pc + 1$
	3	$rA =$ read from memory at the address stored at $pc + 1$

↑  
immediates

Broadly doing work



## Example 3-bit icode

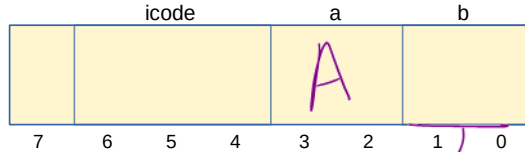
icode	b	meaning
1		$rA += rB$
2		$rA \&= rB$
5	0	$rA = \sim rA$
	1	$rA = -rA$
	2	$rA = !rA$
6	1	$rA +=$ read from memory at $pc + 1$
	2	$rA \&=$ read from memory at $pc + 1$

*Handwritten notes:*  
A purple arrow points from the '!' in the row 'rA = !rA' to the right. To the right of this arrow, the following is written:  
 $!0 = 1$   
 $!1 = 0$

Note: We can implement other operations using these things!

# icode 5 and 6

Special property of icode 5-6: only one register used



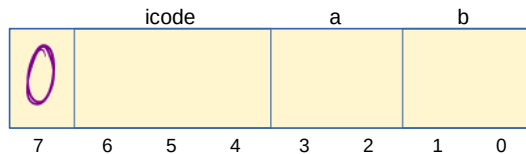
## Example 3-bit icode

icode	b	action
5	0	$rA = \sim rA$
	1	$rA = -rA$
	2	$rA = !rA$
	3	$rA = pc$



# icode 5 and 6

Special property of 5-6: only one register used



- Side effect: all bytes between 0 and 127 are valid instructions!
- As long as high-order bit is 0
- No syntax errors, any instruction given is valid

# Immediate values

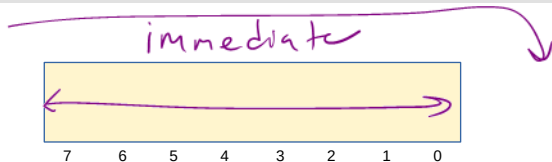
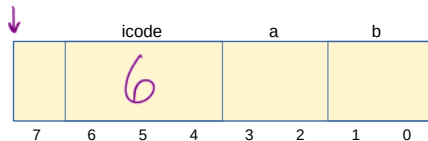
icode 6 provides literals, **immediate** values

$r0 = 23$   
 $x = 23$

## Example 3-bit icode

icode	b	action
6	<u>0</u>	$rA = \text{read from memory at } pc + 1$
	1	$rA += \text{read from memory at } pc + 1$
	2	$rA \&= \text{read from memory at } pc + 1$
	3	$rA = \text{read from memory at the address stored at } pc + 1$

For icode 6, increase  $pc$  by 2 at end of instruction

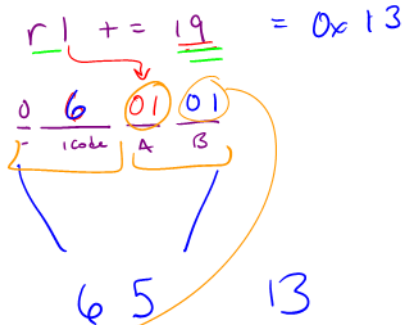


# Encoding Instructions

Example 1: r1 += 19

# Instructions

icode	b	meaning
0		$rA = rB$
1		$rA += rB$
2		$rA \&= rB$
3		$rA =$ read from memory at address $rB$
4		write $rA$ to memory at address $rB$
5	0	$rA = \sim rA$
	1	$rA = -rA$
	2	$rA = !rA$
	3	$rA = pc$
6	0	$rA =$ read from memory at $pc + 1$
	1	$rA +=$ read from memory at $pc + 1$
	2	$rA \&=$ read from memory at $pc + 1$
	3	$rA =$ read from memory at the address stored at $pc + 1$ For icode 6, increase $pc$ by 2 at end of instruction
7		Compare $rA$ as 8-bit 2's-complement to 0 if $rA \leq 0$ set $pc = rB$ else increment $pc$ as normal




# Encoding Instructions

Example 2:  $M[\underline{0x82}] += r3$

Read memory at address `0x82`, add `r3`, write back to memory at same address

# Instructions

icode	b	meaning
0		$rA = rB$
→ 1		$rA += rB$
2		$rA \&= rB$
→ 3		$rA =$ read from memory at address $rB$
4		<u>write</u> $rA$ to memory at address $rB$
5	0	$rA = \sim rA$
	1	$rA = -rA$
	2	$rA = !rA$
	3	$rA = pc$
6	0	$rA =$ read from memory at $pc + 1$
	1	$rA +=$ read from memory at $pc + 1$
	2	$rA \&=$ read from memory at $pc + 1$
	3	$rA =$ read from memory at the address stored at $pc + 1$
		For icode 6, increase $pc$ by 2 at end of instruction
7		Compare $rA$ as 8-bit 2's-complement to 0 if $rA \leq 0$ set $pc = rB$ else increment $pc$ as normal

60 82 34 17 44  


$M[0x82] += r3$   
 $r_0 = 0x82 \rightarrow r_1 = M[r_0]$   
 $r_1 += r3$   
 $M[r_0] = r_1$

$r_0 = x82$        $\underbrace{6 \ 0 \ 0}_{60} \ 82$

$r_1 = M[r_0]$        $\underbrace{3 \ 1 \ 0}_{01 \ 00}$   
 34

$r_1 += r3$        $\underbrace{1 \ 1 \ 3}_{01 \ 11}$

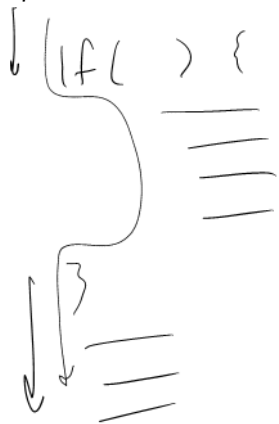
$M[r_0] = r_1$        $\underbrace{4 \ 1 \ 0}_{01 \ 00}$   
 44  
*value* (blue arrow pointing to 1)  
*addr/pc* (red arrow pointing to 0)

# Jumps

- Moves and math are large portion of our code
- We also need **control constructs**
  - Change what we are going to do next
  - **if, while, for**, functions, ...
- Jumps provide mechanism to perform these control constructs
- We jump by assigning a new value to the program counter **PC**

# Jumps

For example, consider an `if`





# Jumps

## Example 3-bit icode

icode	meaning
7	Compare $rA$ as 8-bit 2's-complement to 0 if $\underline{rA} \leq 0$ set $pc = \underline{rB}$ else increment $pc$ as normal

Instruction icode 7 provides a conditional jump

- Real code will also provide an **unconditional** jump, but a conditional jump is sufficient

# Writing Code

We can now write any\* program!

- When you run code, it is being turned into instructions like ours
- Modern computers use a larger pool of instructions than we have (we will get there)

\*we do have some limitations, since we can only represent 8-bit values and some operations may be tedious.

# Our code to this machine code

How do we turn our control constructs into jump statements?

if/else to jump

while to jump

# Function Calls

# Encoding Instructions

Example 3: `if r0 < 9 jump to 0x42`

# Instructions

icode	b	meaning
0		$rA = rB$
1		$rA += rB$
2		$rA \&= rB$
3		$rA =$ read from memory at address $rB$
4		write $rA$ to memory at address $rB$
5	0	$rA = \sim rA$
	1	$rA = -rA$
	2	$rA = !rA$
	3	$rA = pc$
6	0	$rA =$ read from memory at $pc + 1$
	1	$rA +=$ read from memory at $pc + 1$
	2	$rA \&=$ read from memory at $pc + 1$
	3	$rA =$ read from memory at the address stored at $pc + 1$ For icode 6, increase $pc$ by 2 at end of instruction
7		Compare $rA$ as 8-bit 2's-complement to 0 if $rA \leq 0$ set $pc = rB$ else increment $pc$ as normal



# Questions on Multiply

# Encoding Instructions

Example 4: `a <<= b`