ISAs, Stacks, and Endianness

CS 2130: Computer Systems and Organization 1 October 5, 2022

• Homework 4 due Monday, 11pm on Gradescope

Instruction Set Architecture (ISA) is an abstract model of a computer defining how the CPU is controlled by software

- Provides an abstraction layer between:
 - Everything computer is really doing (hardware)
 - What programmer using the computer needs to know (software)
- Hardware and Software engineers have freedom of design, if conforming to ISA
- Can change the machine without breaking any programs

CSO: covering many of the times we'll need to think across this barrier

Backwards compatibility

- Include flexibility to add additional instructions later
- Original instructions will still work
- Same program can be run on PC from 10+ years ago and new PC today

Most manufacturers choose an ISA and stick with it

• Notable Exception: Apple

What about our ISA?

- \cdot Enough instructions to compute what we need
- As is, lot of things that are painful to do
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Our Instruction Set Architecture

icode	b	meaning
0		rA = rB
1		rA += rB
2		rA &= rB
3		${f r}{f A}$ = read from memory at address ${f r}{f B}$
4		write ${f r}{f A}$ to memory at address ${f r}{f B}$
5	0	$rA = \sim rA$
	1	rA = -rA
	2	rA = !rA
	3	rA = pc
6	0	rA = read from memory at pc + 1
	1	rA += read from memory at $pc + 1$
	2	rA &= read from memory at pc + 1
	3	rA = read from memory at the address stored at pc + 1
		For icode 6, increase pc by 2 at end of instruction
7		Compare rA as 8-bit 2's-complement to 0
		if rA <= 0 set pc = rB
		else increment pc as normal

What about real ISAs?

What about our ISA?

- \cdot Enough instructions to compute what we need
- As is, lot of things that are painful to do
 - This was on purpose! So we can see limitations of ISAs early
- Add any number of new instructions using the reserved bit (7)
- Missing something important: *Help to put variables in memory*

So far... we/compiler chose location for variable

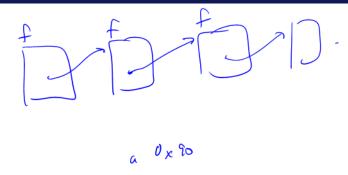
Consider the following example:

Recursion

 \cdot The formal study of a function that calls itself

Storing Variables in Memory

Where do we store **a**?



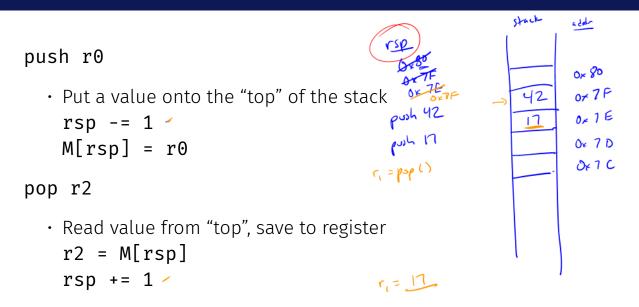
Stack - a last-in-first-out (LIFO) data structure

• The solution for solving this problem

rsp - Special register - the stack pointer

- Points to a special location in memory
- Two operations most ISAs support:
 - **push** put a new value on the stack
 - **pop** return the top value off the stack

The Stack: Push and Pop



The Stack: Push and Pop

push 17 push 12 r = pop () FR FE PP SPE 1 ~ 62 $p_{vol} 37$ $p_{vol} 62$ $p_{vol} 75$ $r_1 = p_0 p_1(1)$ $r_1 = p_0 p_1(2)$ F 75 FD r = 12 78 62

The Stack: Push and Pop

Patents and Copyright

Can we patent our ISA? Should we?

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6	0	rA = read from memory at pc + 1
	1	rA += read from memory at $pc + 1$
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Patents and Copyright

Copyright

• "Everyone is a copyright owner. Once you create an original work and fix it, like taking a photograph, writing a poem or blog, or recording a new song, you are the author and the owner."

from https://www.copyright.gov/what-is-copyright/

Patent

 "Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title." In software and hardware, patents become messy

- \cdot Code is a description of a process we want the computer to do
- Do not have to implement the process to patent it

Question: Should we patent something like our ISA?

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Question: Should we patent something like our ISA?

What is the current state of the art?

How can we get value from what we create?

- Copyright distribute closed source software
- License Agreements (in contract law)
- Always innovate