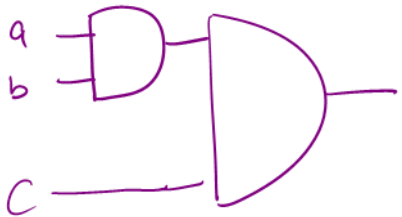
Warm up!

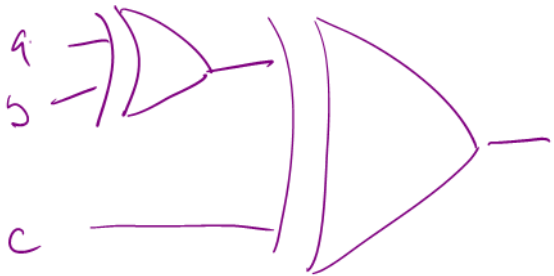
Can I ~~build~~ make an *n*-input AND from 2-input AND gates?

n = 3



Warm up!

What about XOR gates?



Adder, Clocks

CS 2130: Computer Systems and Organization 1

September 9, 2022

Announcements

- Quiz 2 out at 5pm, due Monday at 8am
- Homework 1 due Monday
- New Location! **Gilmer 301** on Monday It's official!

Review

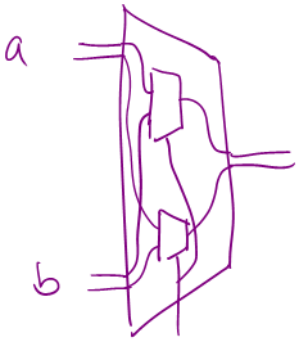
- Transistors
- Information modeled by voltage through wires (1 vs 0)
- Gates

- Examples of AND, NOT gates
- Multi-bit values: representing integers
  - Signed and unsigned
- Floating point

How to do the work of multi-bit?

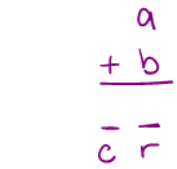
Multi-bit Mux

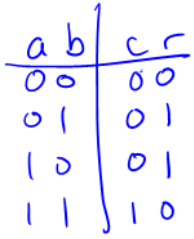
Our first multi-bit example: mux

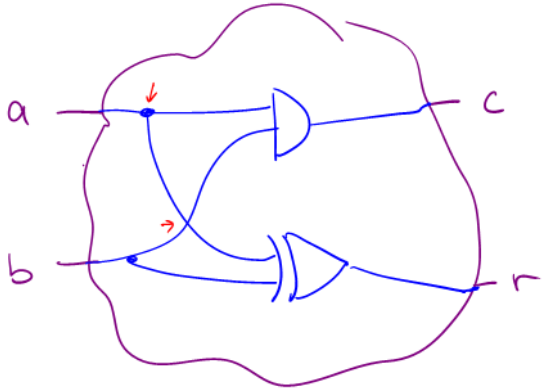


Adder

Add 2 1-bit numbers: a, b

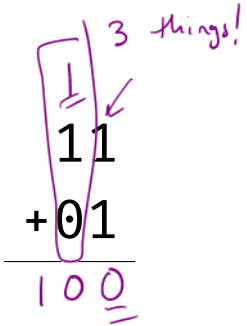






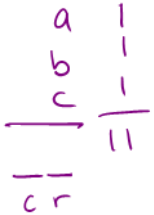
Adder

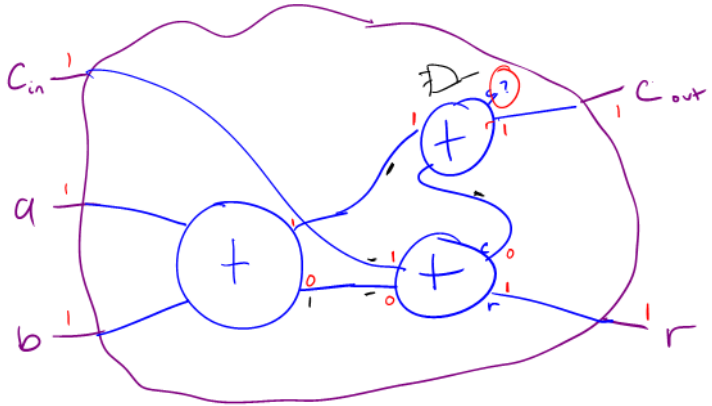
What is missing? Consider:

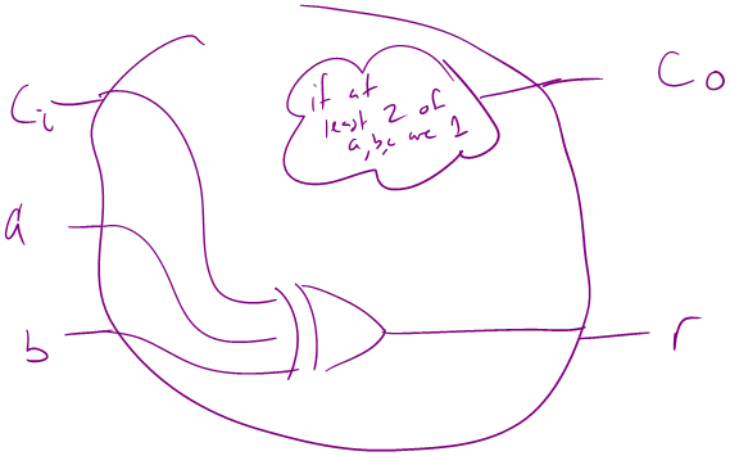


3-input Adder

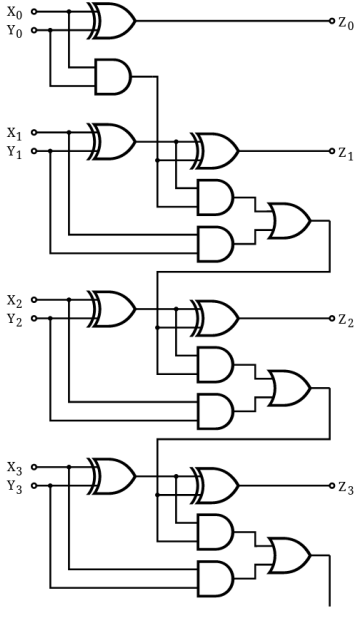
Add 3 1-bit numbers: a, b, c



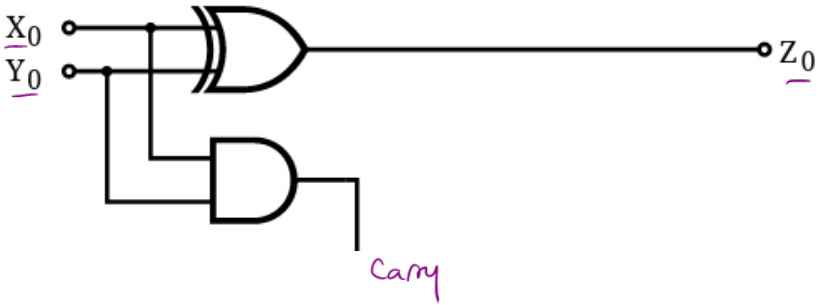



C_i
a
b
if at least 2 of a, b, c are 1
C_o
r

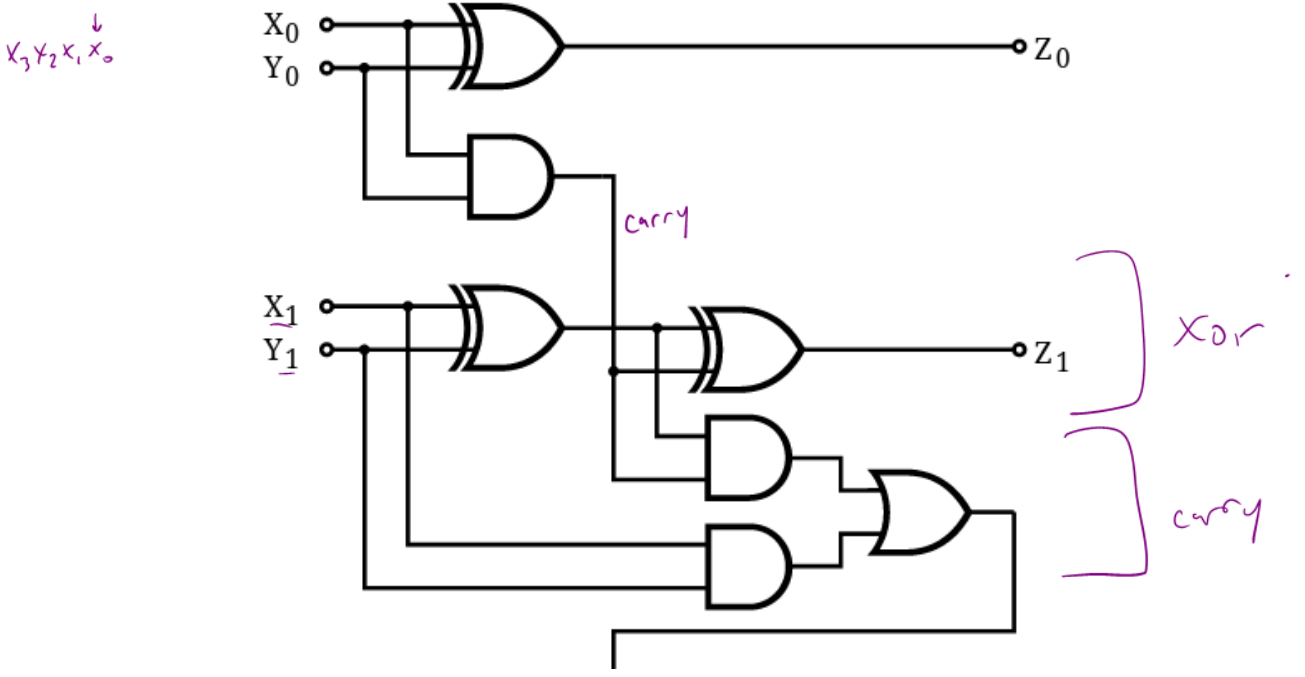
Ripple-Carry Adder



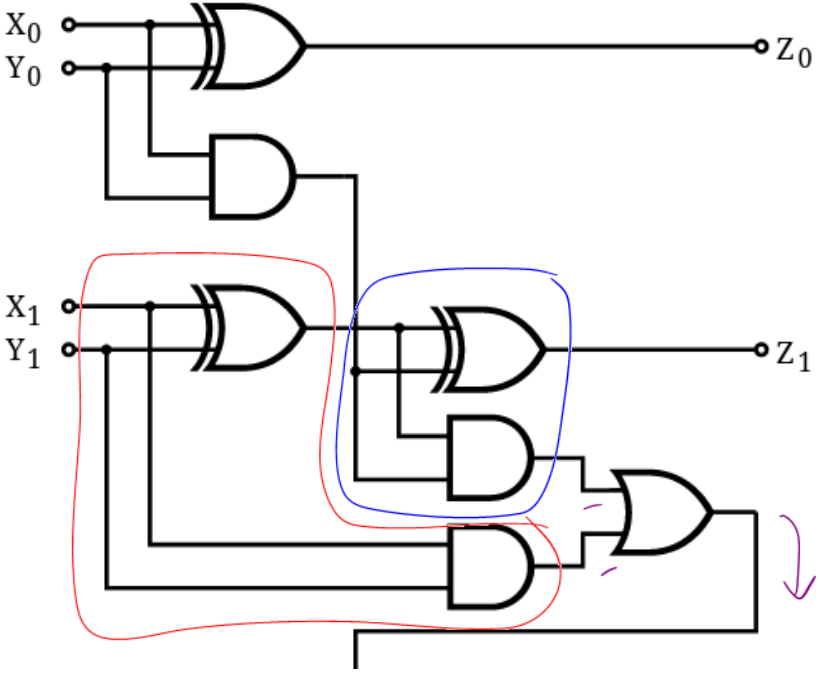
Ripple-Carry Adder



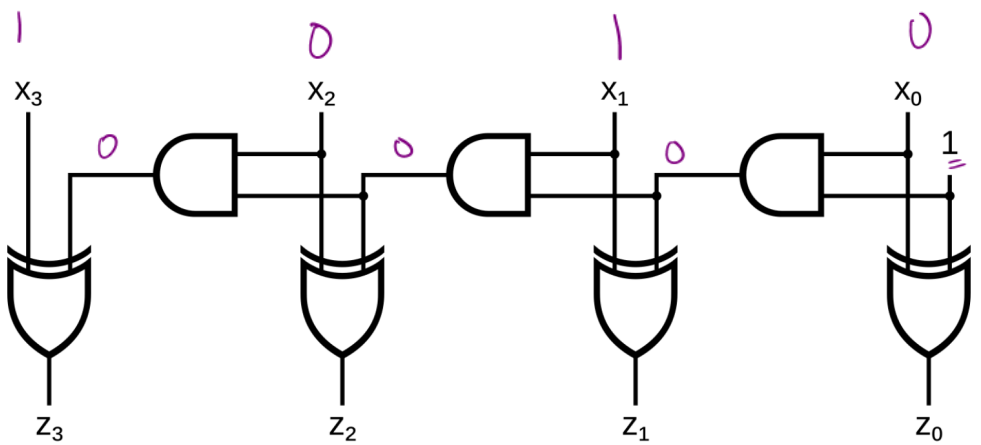
Ripple-Carry Adder



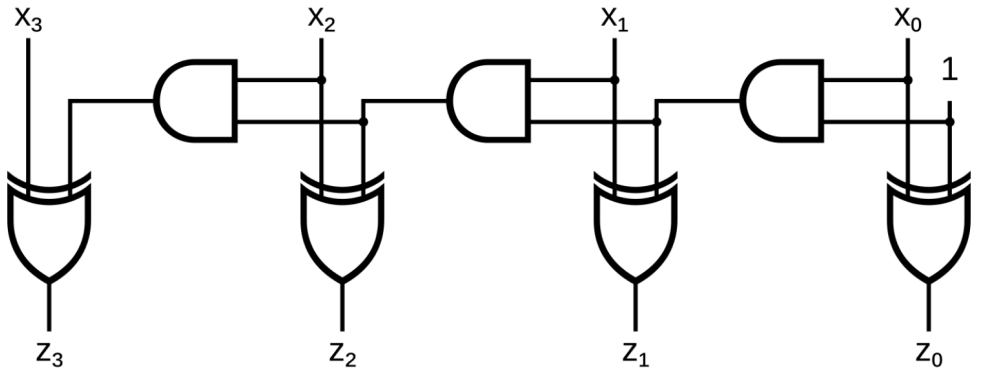
Ripple-Carry Adder



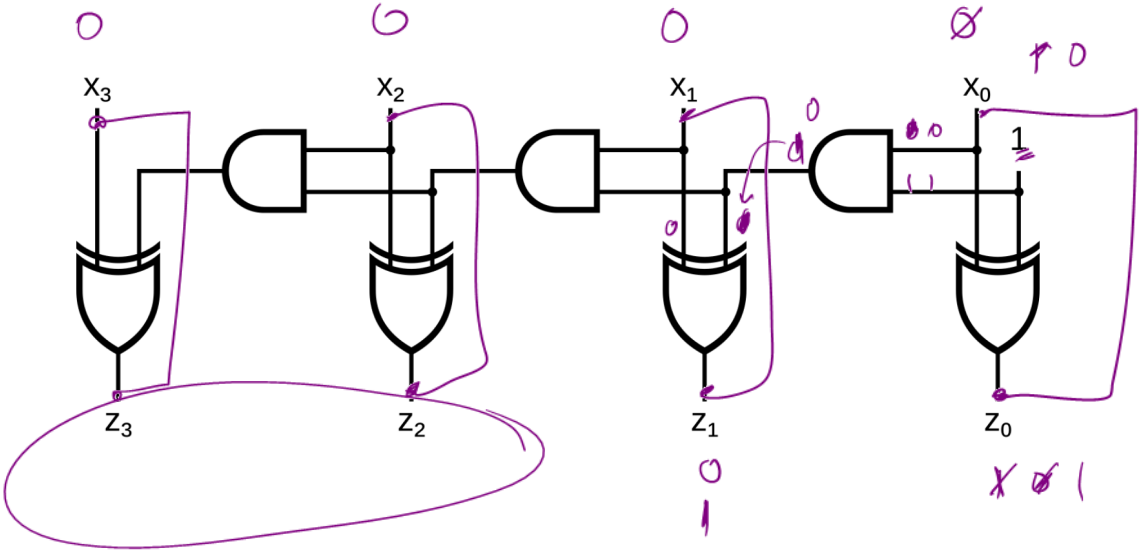
What does this circuit do?



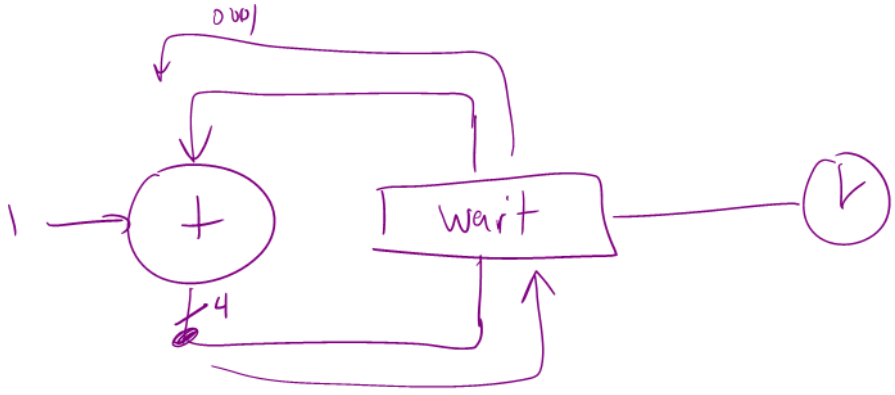
What does this circuit do?



Increment Circuit



Building a Counter



Gate Delay

What happens when I change my input?