

CS2100 DSA
CS2120 DMT

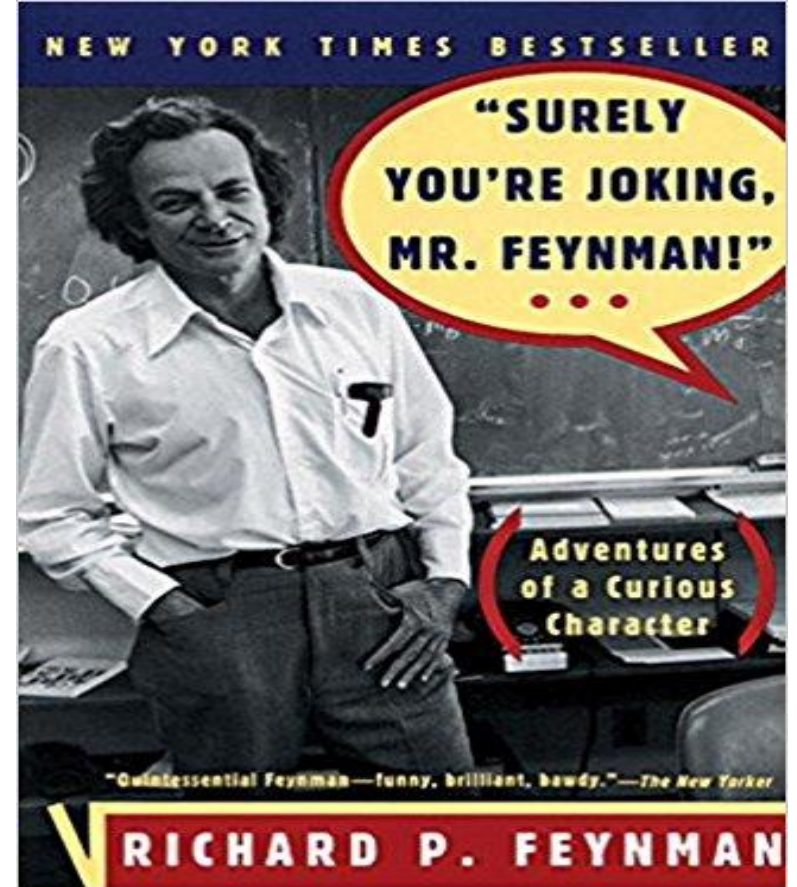


CS 2130

CS01

What makes a computer go?

...there was a book that started out with four pictures: first there was a wind-up toy; then there was an automobile; then there was a boy riding a bicycle; then there was something else. And underneath each picture, it said "What makes it go?"



"What makes it go? Everything goes because the sun is shining."

"The toy goes because the spring is wound up"

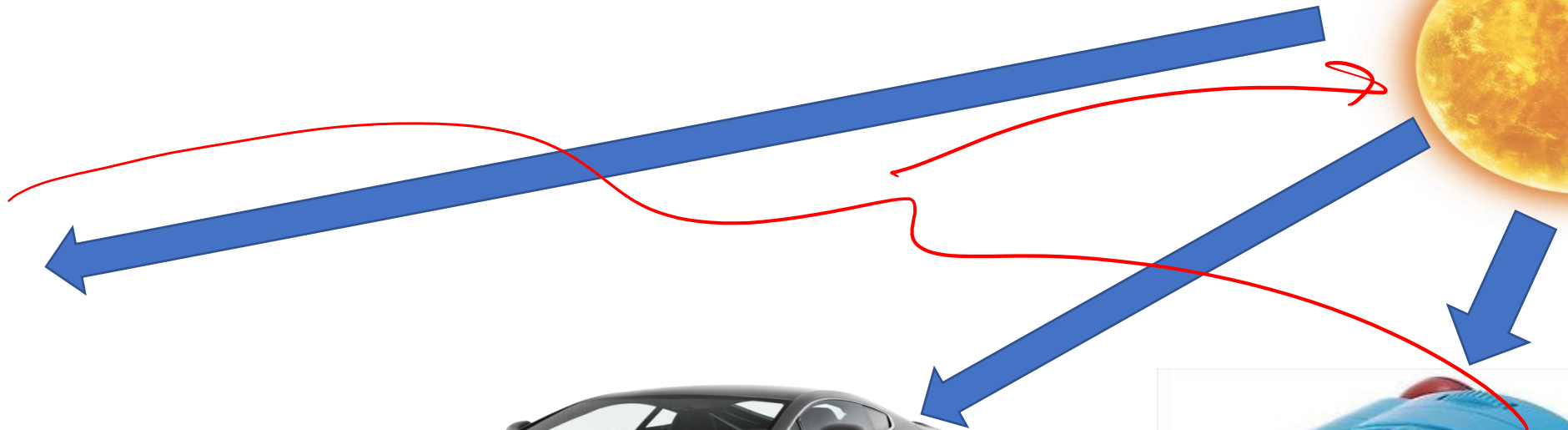
"How did the spring get wound up?"

"I wound it up"

"And how did you get moving?"

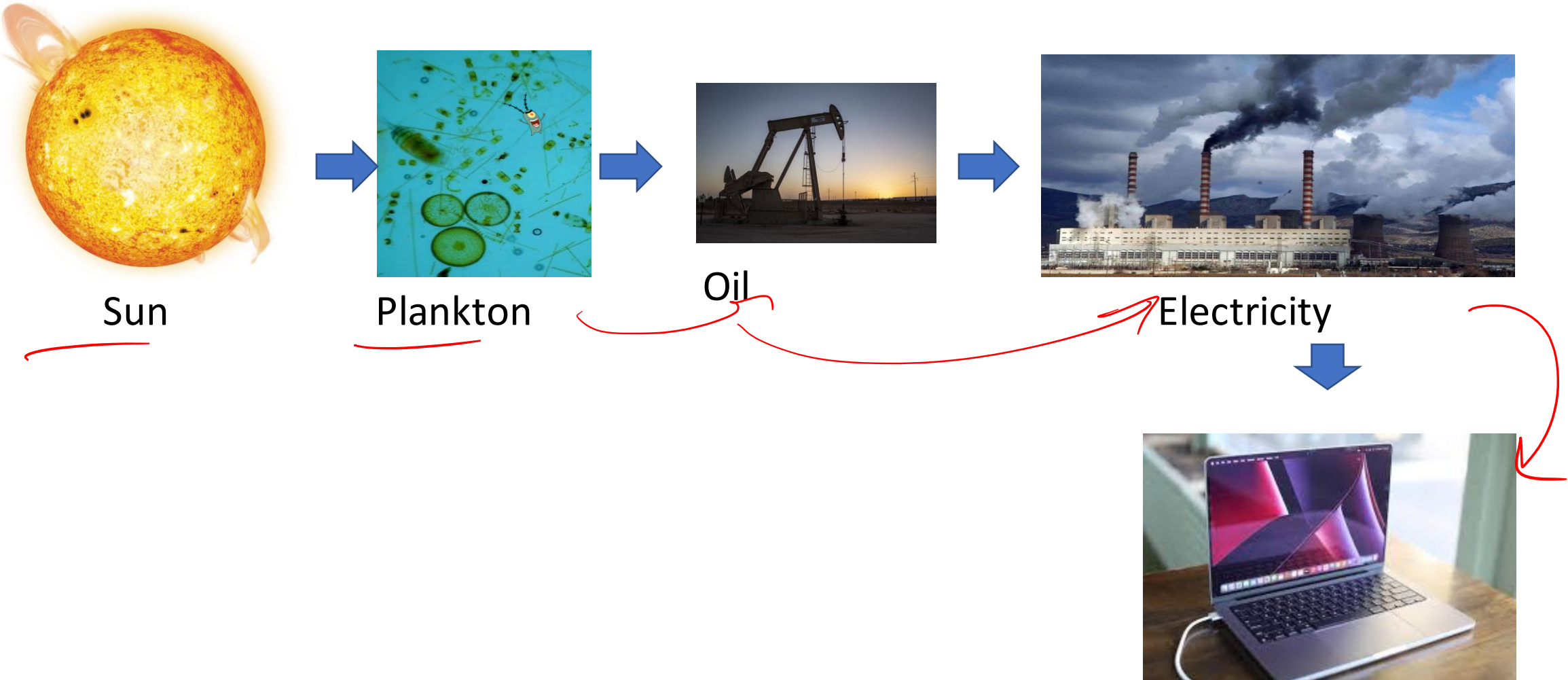
"From eating"

"And food grows only because the sun is shining. So it's because the sun is shining that all these things are moving"



What makes computers go?

One answer:



What makes computers go?

```
Data: this text
Result: how to write algorithm with LATEX2ε
initialization:
while not at end of this document do
  read current;
  if understand then
    go to next section;
    current section becomes this one;
  else
    go back to the beginning of current section;
  end
end
```

Algorithm 1: How to write algorithms

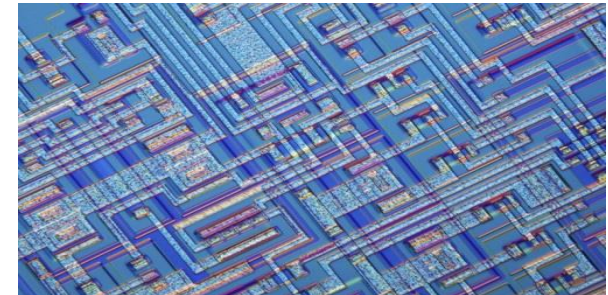
Algorithms

```
for i in people.data.users:
  response = client.api.statuses.user_timeline.get(screen_name=i.screen_name)
  print 'Got', len(response.data), 'tweets from', i.screen_name
  if len(response.data) != 0:
    ldate = response.data[0]['created_at']
    ldate2 = datetime.strptime(ldate, '%a %b %d %H:%M:%S +0000 %Y')
    today = datetime.now()
    howlong = (today - ldate2).days
    if howlong < daywindow:
      print i.screen_name, 'has tweeted in the past', daywindow,
      totaltweets += len(response.data)
      for j in response.data:
        if j.entities.urls:
          for k in j.entities.urls:
            newurl = k['expanded_url']
            urlset.add((newurl, j.user.screen_name))
    else:
      print i.screen_name, 'has not tweeted in the past', daywindow
```

Programs

$$\frac{P, \quad P \Rightarrow Q}{Q}$$

Mathematics



Transistors/memory

2120

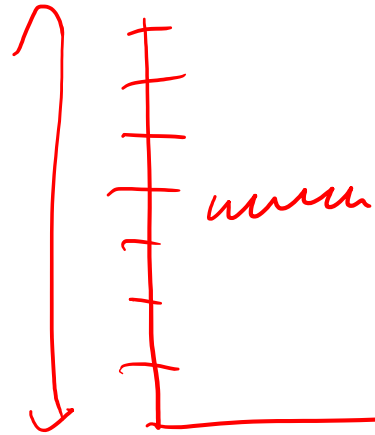
DSA1

CSOA

CS1110

Claude Shannon | Information

01



conclusion: regardless, same amount of info per second
for 2 levels, hardware is easier

George Boole | Boolean Algebra

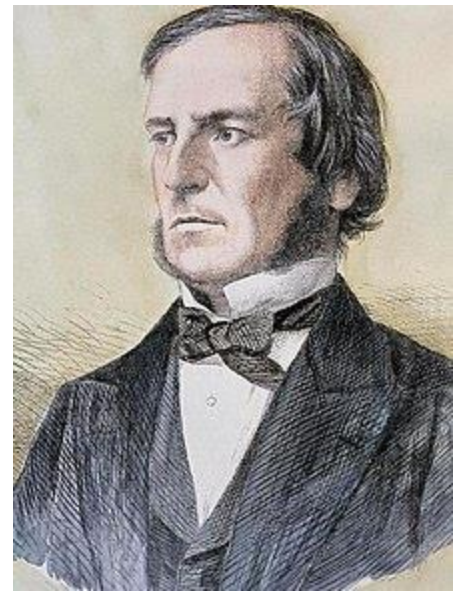
Boolean Algebra

True 1
False 0

and
&&

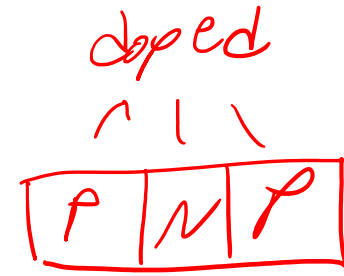
or
||

not
!

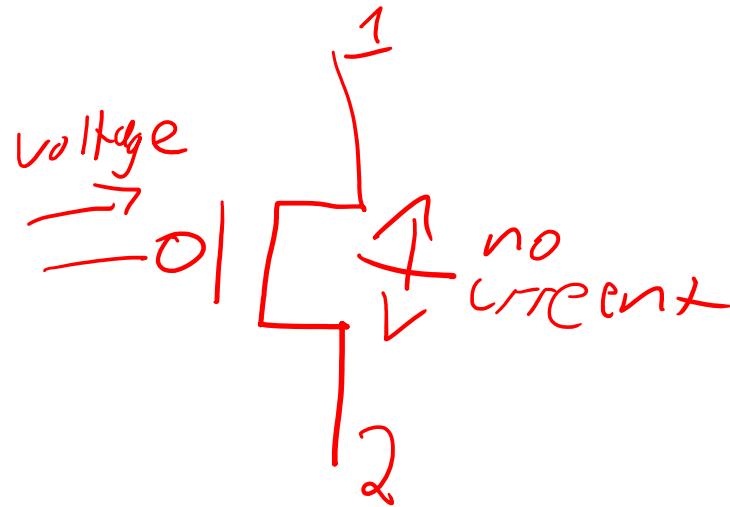
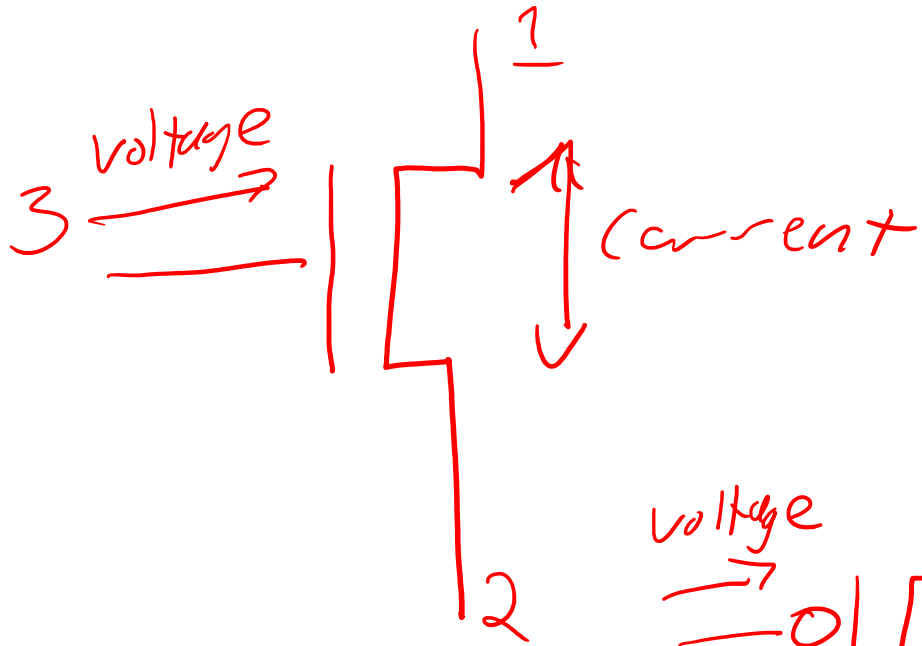


Making a Device

Transistor

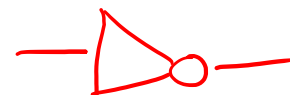
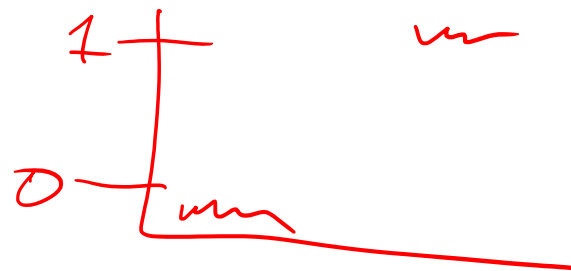
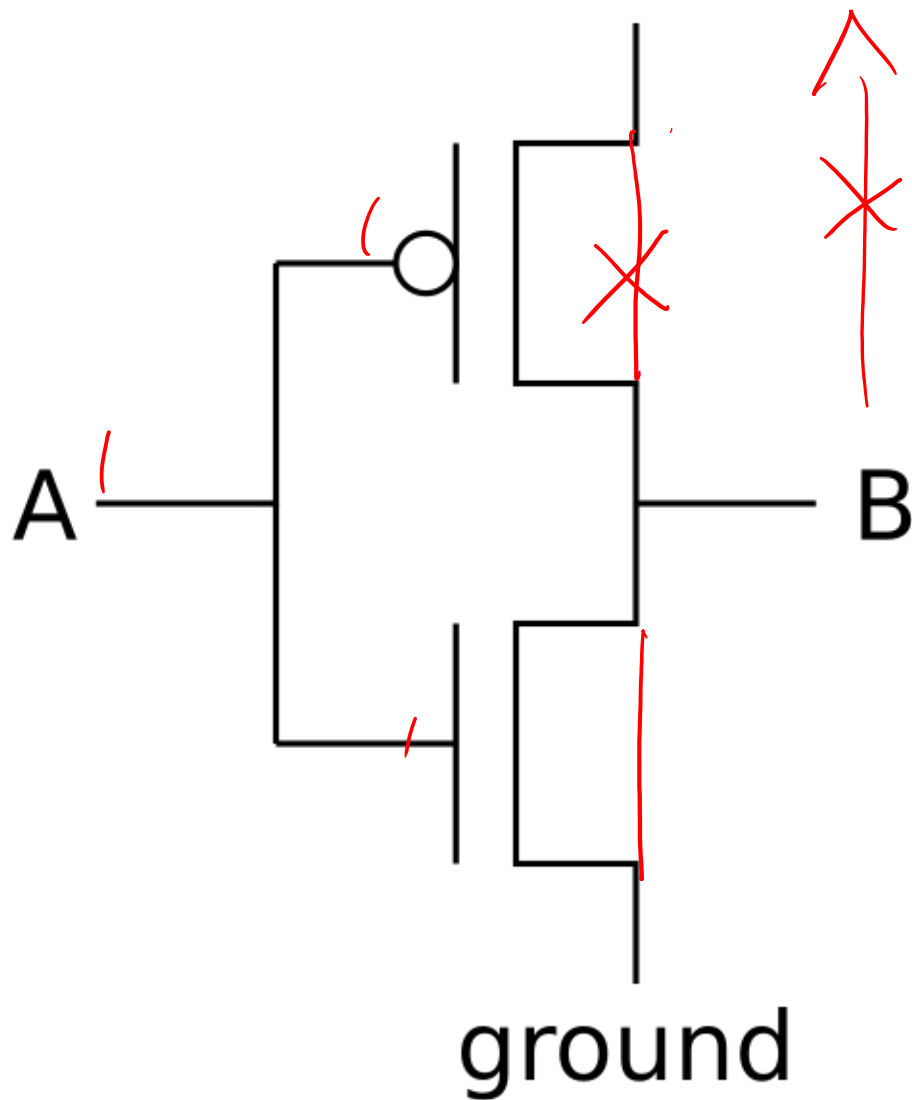


electricity
voltage: pressure
current: rate of flow

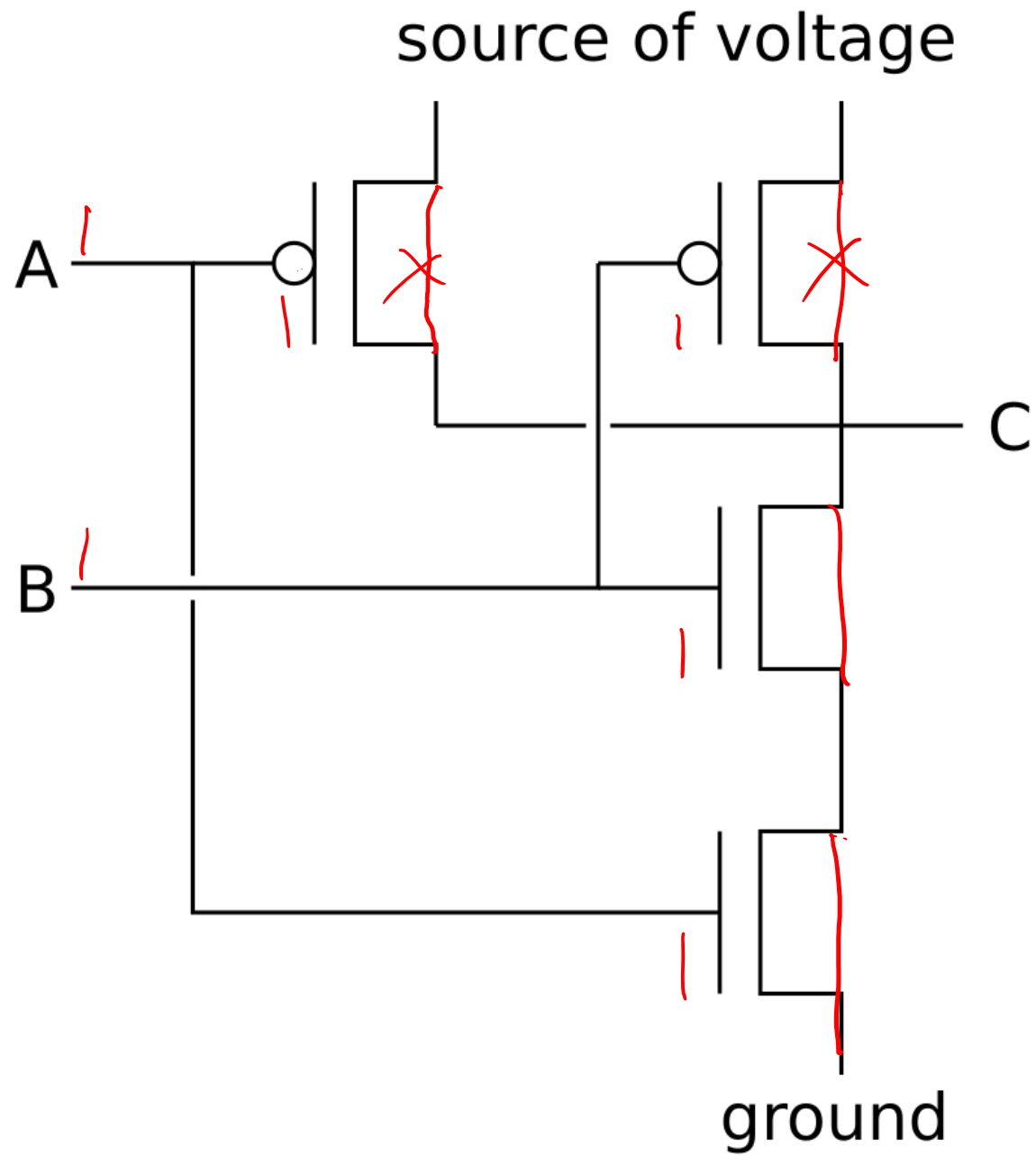


A	B
0	1
1	0

source of voltage ←



A	B	C
00	1	
01	1	
10	1	
11	0	



run

A gate C

= D

Trinary Operator