## CS 2130 Exam 2

## Name

You MUST clearly write your computing ID on EACH page and put your name on the top of this page, too. Please write legibly.

Write your answers in the box labeled "Answer" when provided. In any multiple choice answer, fill in the circle completely for credit; checkmarks, circles, lines, or other marks will be graded as an empty circle.

If you are still writing when "pens down" is called, your exam will not be graded - even if you are still signing the honor pledge. So please do that first. Sorry to have to be strict on this!

There are 6 pages to this exam. Once the exam starts, please make sure you have all the pages. Questions are worth different amounts of points, so be sure to look over all the questions and plan your time accordingly.

This exam is CLOSED text book, closed-notes, closed-cell phone, closed-smart watch, closedcomputer, closed-neighbor, etc. You may not discuss this exam with anyone until after the grades have been released. Please sign the honor pledge below.

Notes: Callee-saved registers: $\% r b x, \% r s p, \% r b p, \% r 12, \% r 13, \% r 14, \% r 15$

On my honor as a student, I have neither given nor received aid on this exam. I will not discuss the content of this exam, even in vague terms, with anyone other than current course staff, until after grades have been released.

## Page 2: Endianness, Backdoors, Patents

1. [8 points] The following 4 bytes are stored sequentially: [ $0 \times a 1,0 \times 7 \mathrm{c}, 0 \times 93,0 \times 40$ ]. If these bytes represent an array of two 16-bit values, what is the value of the second element in the array given each of the following assumptions? Answer in hexadecimal.
A. Assume little-endian storage.
B. Assume big-endian storage.
0x4093
0x9340
2. [4 points] In class, we discussed copyright and patents. Why would copyright of the website describing our Toy ISA from class not be sufficient intellectual property protection to prevent others from creating clones?

It allows copying of the description and re-implementation of the functionality
O It restricts re-implementation of the functionality, but not copying of the descriptionIt restricts copying of the description, but not re-implementation of the functionalityIt would actually be sufficient protection

## C

3. [4 points] We discussed a method to create a backdoor in our Toy ISA processor. What was true of our malicious payload? (Fill in the circle completely for all that apply.)It must be executed by the user.
$\bigcirc$ It must be loaded into memory and read in order.It contains a passcode and code we want to run.


It contains only a passcode.
It starts with the code we want to run.

B,C
4. [4 points] What is true of hardware backdoors like the one we discussed?


They require a large number of gates relative to the number of other gates on the chip.
They are easy to identify upon inspection of the hardware.


Any code the exploit runs must be compiled into the hardware.
$\bigcirc$
The backdoor might only be found after it is exploited.

D

## Page 3: Reading Assembly

5. [24 points] Assume the first eight registers and the given segment of memory have the following values before the next few instructions.

| Register | Value (hex) |
| :---: | ---: |
| rax | $0 \times 100000040$ |
| rcx | $0 \times 1000000 \mathrm{ff}$ |
| $r d x$ | $0 \times 4$ |
| rbx | $0 \times 2130000000$ |
| rsp | $0 \times 8 \mathrm{fffb} 8$ |
| rbp | $0 \times 8 \mathrm{fffb0}$ |
| rsi | $0 \times 10$ |
| rdi | $0 \times 1025$ |


| Mem Addr. | Value (hex) |
| :---: | ---: |
| $0 \times 8 \mathrm{fffb} 0$ | $0 \times 43$ |
| $0 \times 8 \mathrm{fffb} 1$ | $0 \times 4 \mathrm{f}$ |
| $0 \times 8 \mathrm{fffb} 2$ | $0 \times 15$ |
| $0 \times 8 \mathrm{fffb} 3$ | $0 \times 1 \mathrm{a}$ |
| $0 \times 8 \mathrm{fffb} 4$ | $0 \times \mathrm{ab}$ |
| $0 \times 8 \mathrm{fffb} 5$ | $0 \times 8 \mathrm{a}$ |
| $0 \times 8 \mathrm{fffb} 6$ | $0 \times \mathrm{f}$ |
| $0 \times 8 \mathrm{fffb} 7$ | $0 \times 42$ |
| $0 \times 8 \mathrm{fffb} 8$ | $0 \times 11$ |


| Mem Addr. | Value (hex) |
| :---: | ---: |
| $0 \times 8 \mathrm{fffb} 9$ | $0 \times 34$ |
| $0 \times 8 \mathrm{fffba}$ | $0 \times 05$ |
| $0 \times 8 \mathrm{fffbb}$ | $0 \times 45$ |
| $0 \times 8 \mathrm{fffbc}$ | $0 \times b \mathrm{f}$ |
| $0 \times 8 \mathrm{fffbd}$ | $0 \times 19$ |
| $0 \times 8 \mathrm{fffbe}$ | $0 \times 33$ |
| $0 \times 8 \mathrm{fffb}$ | $0 \times 27$ |
| $0 \times 8 \mathrm{fffc} 0$ | $0 \times 9 \mathrm{a}$ |
| $0 \times 8 \mathrm{fffc} 1$ | $0 \times 4 \mathrm{f}$ |

Which program registers are modified, and to what values, by the following instructions? Leave spaces blank if fewer registers change than there are lines. If no registers are changed, write "none" in the first register box with no new value. Each instruction below is independent; do not use the result of one as input for the next. (4 points each)

rdx/edx, 0x45053411
testq \%rdx, \%rdi

| Register | New Value |
| :---: | :---: |
|  |  |
|  |  |

none (or conditional flags)
popw \%ax

| Register | New Value |
| :---: | :---: |
|  |  |
|  |  |

rax/eax/ax 0x3411, rsp 0x8fffba
leaq $0 x 8(\% r b p), \% r d x$

| Register | New Value |
| :---: | :---: |
|  |  |
|  |  |

rdx, 0x8fffb8
andl $-0 x 10(\% r s p, \% r d x, 2)$, \%ecx

| Register | New Value |
| :---: | :---: |
|  |  |
|  |  |

rcx/ecx, 0x43
callq foo

| Register | New Value |
| :---: | :---: |
|  |  |
|  |  |

rsp, 0x8fffb0

## Page 4: Compilation Pipeline and Writing Assembly

6. [8 points] For each of the following bugs, indicate the stage of compilation that would find it. If it would not be found until run-time, write "none". The stages are:

- Lexing - breaking the input into words and related tokens
- Parsing - making a parse tree (an abstract syntax tree (AST))
- Type-checking - annotating the AST with data types, etc
- Code generation - creating assembly
- Assembling - turning assembly into machine code
- Linking - attaching library files to code
A. Adding an int to a string, such as
int $x=a+2$;

```
const char *a = "hello";
```

B. Calling a function, such as mynewfunct iontha,t does not exist at all.

## T, Linking

7. [19 points] Consider the following C code snippet:
```
long exammath(long a, const char *c) {
    long i = 0;
    while (i <= 30) {
        a -= a;
        i += 1;
    }
    printf(c, i);
    return a;
}
```

Rearrange the x86-64 assembly instructions on the next page to implement the exammath function. The instructions have been grouped into three sections; for each section, reorder the instructions by writing the number of the appropriate instruction on the lines provided to the right. Some order has been provided for you and the order of instructions in one section may influence the ordering of instructions in another section. Each instruction may be used at most once and only within its group. Some instructions may not be used. All blanks will be filled. When complete, your reordered instructions (under "Proper Ordering") should fully implement exammath.

## Page 5: Writing Assembly (continued)

Proper Ordering

1. exammath:
1 exammath:
2. movq $\$ 30$, \%r12 $\qquad$ 3
3. pushq \%r12 $\qquad$ 4
4. pushq \%rbx
$\qquad$ 2
5. xorl \%ebx, \%ebx
$\qquad$ 5
6. addq \$1, $\% r b x$ $\qquad$ 10
7. cmpq $\% \mathrm{rbx}$, $\% \mathrm{r} 12$ $\qquad$ 6 or 11
8. jge label1 $\qquad$ 11 or 6
9. jle label1 $\qquad$ 7
10. label1: $\qquad$ 8
11. subq \%rdi, \%rdi
12. callq printf $\qquad$ 18
13. movq \%rbx, \%rsi $\qquad$
14. movq \%rsi, \%rdi $\qquad$
15. popq \%r12

12 callq printf
16. popq \%rax $\qquad$
17. popq \%rbx

17 popq \%rbx
18. pushq \%rdi

15 popq \%r12
19. retq

19 retq

## Page 6: C

8. [12 points] Consider the following $C$ code:
```
char first[5] = {'f', 'Y', 'i', ' !', '\0'};
char *second = strdup("hello");
char *both[2] = {first, second};
```

What is printed for each of the following lines? If the program would crash or seg fault, write crash. Hint: printf("\%c", x); means "print the char stored in variable x."
A. printf("\%c", (*both) [1]);
B. printf("\%c", *(both[1]));
C. puts(\&both[0][2]);
$y, h, i!$
9. [5 points] Assuming that you have written C code in a file named exam2.c, what is the full command on the CS portal to compile your code with debugging information?

```
mst3k@portal01:~$
```

clang -g exam2.c
10. [12 points] Complete the following $C$ function that takes an array of signed integers (arr) and a length ( n ) as parameters, replaces any of the first n elements in the array which are less than 0 with increasing powers of two, and returns the next power of two. For example, if $\mathrm{n}=5$ and arr contains $[0,-9,5,-3,-2]$, the function would update arr to have values [0, 1, 5, 2, 4] and return 8.

```
int powersOfTwo(int *arr, unsigned int n) {
    int twos = 1;
    // Complete this function
    for (int i = 0; i < n; i++) {
        if (arr[i] < 0) {
            arr[i] = twos;
            twos += twos; // double twos
        }
    }
    return twos;
}
```

