

Toy Instruction Set Architecture

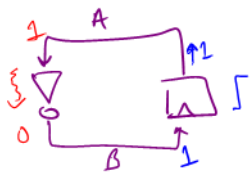
CS 2130: Computer Systems and Organization 1

February 13, 2023

Announcements

- Homework 2 due tonight at 11pm on Gradescope
- Homework 3 out, due next Monday at 11pm on Gradescope

Quiz Review



$$\begin{array}{l}
 0x\text{bebe9029} \rightarrow \\
 8\text{ 0F0F0F0F} \\
 \xrightarrow{\gg 4} \\
 0x\text{0e0e0009} \\
 + 0x\text{0b0b0902} \\
 \hline
 \end{array}$$

$\underbrace{\hspace{1cm}} \quad \underbrace{\hspace{1cm}}$
 $\downarrow \quad \downarrow$
 $\underbrace{\hspace{1cm}} \quad \underbrace{\hspace{1cm}}$

$$-010110000$$

~~$$.1011$$~~

$$.1011 \times 2^7$$

$$\begin{array}{r}
 \underline{1} \quad 1110 \quad 011 \\
 \quad 0111 \\
 + \quad 0111 \\
 \hline
 \quad 1110
 \end{array}$$

$$a = 1_0 = 0 \dots 01$$

$$b = n_0 = 11 \dots 11$$

$$c = a \ll (b \& 31) = 100 \dots 0$$

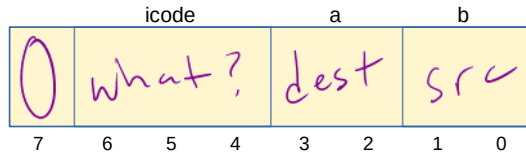
$$x = c + 1 = 1000 \dots 001$$

$$\begin{array}{l}
 (11 \dots 1) 111111 \\
 8\text{ 0000 011111}
 \end{array}$$

Encoding Instructions

Encoding of Instructions

- 3-bit icode (which operation to perform)
 - Numeric mapping from icode to operation
- Which registers to use (2 bits each)
- Reserved bit for future expansion



High-level Instructions

In general, 3 kinds of instructions

- **moves** - move values around without doing “work”
- **math** - broadly doing “work”
- **jumps** - jump to a new place in the code

Moves

Few forms

$$r2 = r3$$

$$r0 = M[R[2]]$$

- Register to register (icode 0), $x = y$
- Register to/from memory (icodes 3-4), $x = M[b]$, $M[b] = x$

$$M[R[2]] = r2$$

Memory

- **Address:** an index into memory.
 - Addresses are just (large) numbers
 - Usually we will not look at the number and trust it exists and is stored in a register

Moves

Toy ISA 3-bit icode

icode	b	action
0		$rA = rB$
3		$rA =$ read from memory at address rB
4		write rA to memory at address rB
5	3	$rA = pc$ ←
6	0	$rA =$ read from memory at $pc + 1$
	3	$rA =$ read from memory at the address stored at $pc + 1$

Broadly doing work

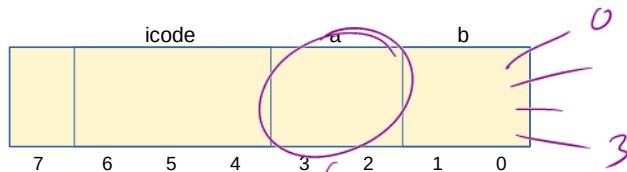
Toy ISA 3-bit icode

icode	b	meaning
1		$rA += rB$
2		$rA \&= rB$
5	0	$rA = \sim rA$
	1	$rA = -rA$
	2	$rA = !rA$
6	1	$rA +=$ read from memory at $pc + 1$
	2	$rA \&=$ read from memory at $pc + 1$

Note: We can implement other operations using these things!

icode 5 and 6

Special property of icode 5-6: only one register used

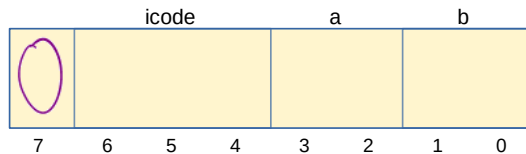


Toy ISA 3-bit icode

icode	b	action
5	0	$rA = \sim rA$
	<u>1</u>	$rA = \underline{-}rA$
	<u>2</u>	$rA = \underline{!}rA$
	<u>3</u>	$rA = \underline{pc}$

icode 5 and 6

Special property of 5-6: only one register used



72
63
0F

- Side effect: all bytes between 0 and 127 are valid instructions!
- As long as high-order bit is 0
- No syntax errors, any instruction given is valid

Immediate values

icode 6 provides literals, immediate values

int x = 5;

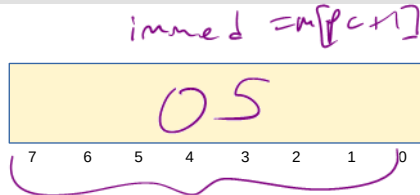
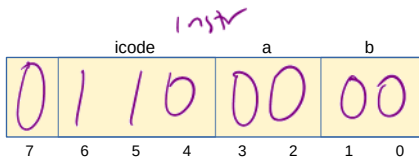
Toy ISA 3-bit icode

icode	b	action
6	0	$rA = \text{read from memory at } pc + 1$
	1	$rA += \text{read from memory at } pc + 1$
	2	$rA \&= \text{read from memory at } pc + 1$
	3	$rA = \text{read from memory at the address stored at } pc + 1$

For icode 6, increase pc by 2 at end of instruction

r0 = 05

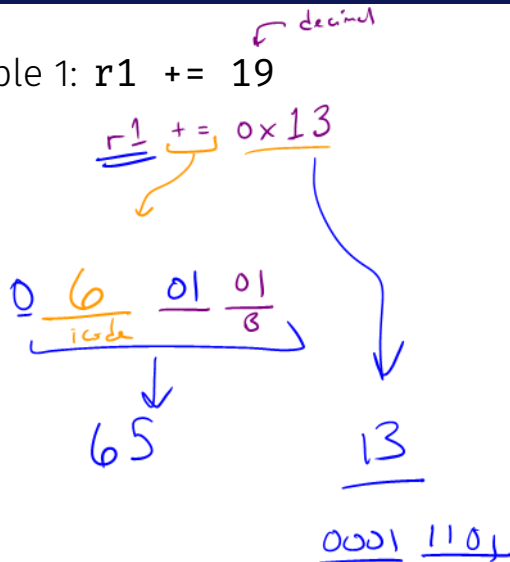
rA = M[M[pc+1]]



Encoding Instructions

icode	b	meaning
0		$rA = rB$
1		$rA += rB$
2		$rA \&= rB$
3		$rA =$ read from memory at address rB
4		write rA to memory at address rB
5	0	$rA = \sim rA$
	1	$rA = -rA$
	2	$rA = !rA$
	3	$rA = pc$
6	0	$rA =$ read from memory at $pc + 1$
	1	$rA +=$ read from memory at $pc + 1$ <i>immed</i>
	2	$rA \&=$ read from memory at $pc + 1$
	3	$rA =$ read from memory at the address stored at $pc + 1$
		For icode 6, increase pc by 2 at end of instruction
7		Compare rA as 8-bit 2's-complement to 0 if $rA \leq 0$ set $pc = rB$ else increment pc as normal

Example 1: $r1 += 19$



Encoding Instructions

icode	b	meaning
0		$rA = rB$
1		$rA += rB$
2		$rA \&= rB$
3		$rA =$ read from memory at address rB
4		write rA to memory at address rB
5	0	$rA = \sim rA$
	1	$rA = -rA$
	2	$rA = !rA$
	3	$rA = pc$
6	0	$rA =$ read from memory at $pc + 1$
	1	$rA +=$ read from memory at $pc + 1$
	2	$rA \&=$ read from memory at $pc + 1$
	3	$rA =$ read from memory at the address stored at $pc + 1$
		For icode 6, increase pc by 2 at end of instruction
7		Compare rA as 8-bit 2's-complement to θ if $rA \leq \theta$ set $pc = rB$ else increment pc as normal

Ex 2: $M[0x82] += r3$

Read memory at address $0x82$, add $r3$,
write back to memory at same address

Jumps

- Moves and math are large portion of our code
- We also need **control constructs**
 - Change what we are going to do next
 - **if, while, for**, functions, ...
- Jumps provide mechanism to perform these control constructs
- We jump by assigning a new value to the program counter **PC**

Jumps

For example, consider an `if`

Jumps

Toy ISA 3-bit icode

icode	meaning
7	Compare rA as 8-bit 2's-complement to θ if $rA \leq \theta$ set $pc = rB$ else increment pc as normal

Instruction icode 7 provides a **conditional** jump

- Real code will also provide an **unconditional** jump, but a conditional jump is sufficient

Writing Code

We can now write any* program!

- When you run code, it is being turned into instructions like ours
- Modern computers use a larger pool of instructions than we have (we will get there)

*we do have some limitations, since we can only represent 8-bit values and some operations may be tedious.

Our code to this machine code

How do we turn our control constructs into jump statements?

if/else to jump

while to jump

Function Calls

Encoding Instructions

icode	b	meaning
0		$rA = rB$
1		$rA += rB$
2		$rA \&= rB$
3		$rA =$ read from memory at address rB
4		write rA to memory at address rB
5	0	$rA = \sim rA$
	1	$rA = -rA$
	2	$rA = !rA$
	3	$rA = pc$
6	0	$rA =$ read from memory at $pc + 1$
	1	$rA +=$ read from memory at $pc + 1$
	2	$rA \&=$ read from memory at $pc + 1$
	3	$rA =$ read from memory at the address stored at $pc + 1$
		For icode 6, increase pc by 2 at end of instruction
7		Compare rA as 8-bit 2's-complement to 0 if $rA \leq 0$ set $pc = rB$ else increment pc as normal

Ex 3: `if r0 < 9 jump to 0x42`